

JEPC

9	7	5	3	1
10	8	6	4	2

BB

9	7	5	3	1
10	8	6	4	2

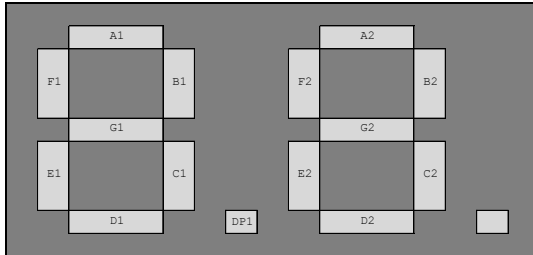
DE-15F VGA



PWR 7.5VDC @ 1A



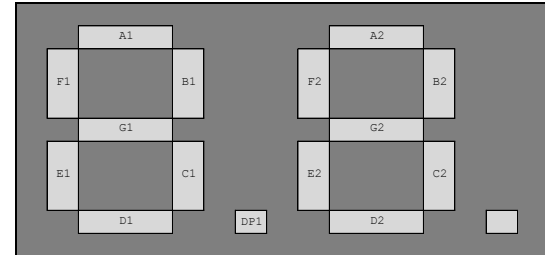
LED2



LED3



LED1



GND



3.3V



5V

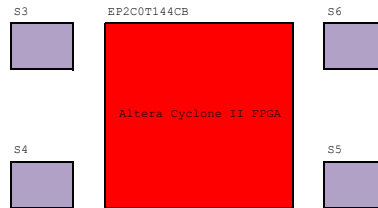


		LL			JP4	
		1	2		1	
DIP Switches S1	S1_PIN1	3	4	P79	2	
	S1_PIN2	5	6	P75	3	
	S1_PIN3	7	8	P74	4	
	S1_PIN4	9	10	P73	5	
	S1_PIN5	11	12	P72	6	
	S1_PIN6	13	14	P112	7	
	S1_PIN7	15	16	P113	8	
	S1_PIN8	17	18	P114	9	
VGA	RED	19	20	P115	10	
	GREEN	21	22	P118	11	
	BLUE	23	24	P119	12	
	VSYN	25	26	P120	13	
7-Segment LED2 (LEFT Char)	LED2_A1	27	28	P121	14	
	LED2_B1	29	30	P122	15	
	LED2_C1	31	32	P125	16	
	LED2_D1	33	34	P126	17	
	LED2_E1	35	36	P129	18	
	LED2_F1	37	38	P132	19	
	LED2_G1	39	40	P133	20	

FPGA

		LM			JP5	
		1*	2*		1	
PS/2	PS_CLK	3*	4*	P141	2	
	PS_DATA	5	6	P142	3	
Tactile Switches	(T-L) S3	7	8	P4	4	
	(B-L) S4	9	10	P7	5	
	(B-R) S5	11	12	P8	6	
	(T-R) S6	13	14	P9	7	
7-Segment LED2 (RIGHT Char + Decimal)	LED2_A2	15	16	P24	8	
	LED2_B2	17	18	P25	9	
	LED2_C2	19	20	P28	10	
	LED2_D2	21	22	P30	11	
	LED2_E2	23	24	P31	12	
	LED2_F2	25	26	P32	13	
	LED2_G2	27	28	P40	14	
	LED2_DP1	29	30	P41	15	
		31	32	P42	16	

FPGA



CLKs

1	2	3	4	5	6	7	8
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F17	F18	F21	F22	F91	F90	F89	F88
2	C	C	C	C	C	C	C
5	L	L	L	L	L	L	L
.	O	O	O	O	O	O	O
1	C	C	C	C	C	C	C
7	K	K	K	K	K	K	K
5	1	2	3	4	5	6	7

M
H
Z

		RM			
		1	2		
JP7	P135	3	4	LED3_P6	LED Bank LED3 (6-10)
	P104	5	6	LED3_P7	
	P103	7	8	LED3_P8	
	P101	9	10	LED3_P9	
	P100	11	12	LED3_P10	7-Segment LED1 (RIGHT Char + Decimal)
	P99	13	14	LED1_G2	
	P97	15	16	LED1_F2	
	P96	17	18	LED1_E2	
	P94	19	20	LED1_D2	
	P93	21	22	LED1_C2	
	P92	23	24	LED1_B2	
	P87	25	26	LED1_A2	
JP6	P86	27	28	LED1_DP1	UART
	P136	29	30	FTDI_TXD	
	P137	31*	32*	FTDI_RXD	
	P139			P143	FPGA

FPGA

		RL			
		1	2		
JP6	P71	3	4	S2_PIN1	DIP Switches S2
	P70	5	6	S2_PIN2	
	P69	7	8	S2_PIN3	
	P67	9	10	S2_PIN4	
	P65	11	12	S2_PIN5	LED Bank LED3 (1-5)
	P64	13	14	S2_PIN6	
	P63	15	16	S2_PIN7	
	P60	17	18	S2_PIN8	
FPGA	P59	19	20	LED3_P1	7-Segment LED1 (LEFT Char)
	P58	21	22	LED3_P2	
	P57	23	24	LED3_P3	
	P55	25	26	LED3_P4	
	P53	27	28	LED3_P5	
	P52	29	30	LED1_G1	
	P51	31	32	LED1_F1	
	P48	33	34	LED1_E1	
	P47	35	36	LED1_D1	
	P45	37	38	LED1_C1	
	P44	39	40	LED1_B1	
	P43			LED1_A1	

* **WARNING:** DO NOT jumper these pin pairs together!

UF-4712 FPGA Dev Board v1.1 Layout & Pinout
Art King, University of Florida, Spring 2011

S1



PS/2



USB Type-B



S2



FTDI_GPIO

5	4	3	2	1
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